



AF/1fw
2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction
Edge and Method of Manufacturing
the Same

§
§
§
§
§
§
§
§
§
§

Group Art Unit: 2815

Examiner: Nguyen, Joseph H.

Atty. Docket: MICS:0114
02-1010

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

CERTIFICATE OF TRANSMISSION OR MAILING
37 C.F.R. 1.8

I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office in accordance with 37 C.F.R. 1.6(d) or is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on the date below:

August 5, 2005

Date

Kerri Hyland
Kerri Hyland

RESPONSE TO FINAL OFFICE ACTION

In response to the Final Official Action mailed on July 14, 2005, Applicants respectfully request reconsideration of the above-identified application in view of the remarks set forth below.